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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Megahed, et al.

Serial No.: 09/930,747

Filed: August 14, 2001

For: Structure And Method For  
Fabrication Of A Leadless Chip Carrier  
With Embedded Inductor

Art Unit: 2900

Examiner: Unknown

**TRANSMITTAL OF FORMAL DRAWINGS**

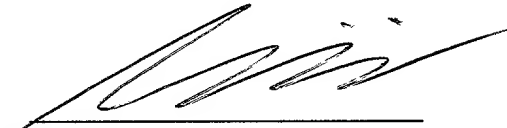
Assistant Commissioner for Patents  
Initial Patent Examination Division  
Washington, D.C. 20231

Dear Madam /Sir:

In response to the Notice To File Corrected Application Papers dated  
September 27, 2001, enclosed are eight (8) sheets of formal drawings for the  
above-identified application.

Respectfully Submitted;  
FARJAMI & FARJAMI LLP

Date: 10/10/01

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